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FIG.1

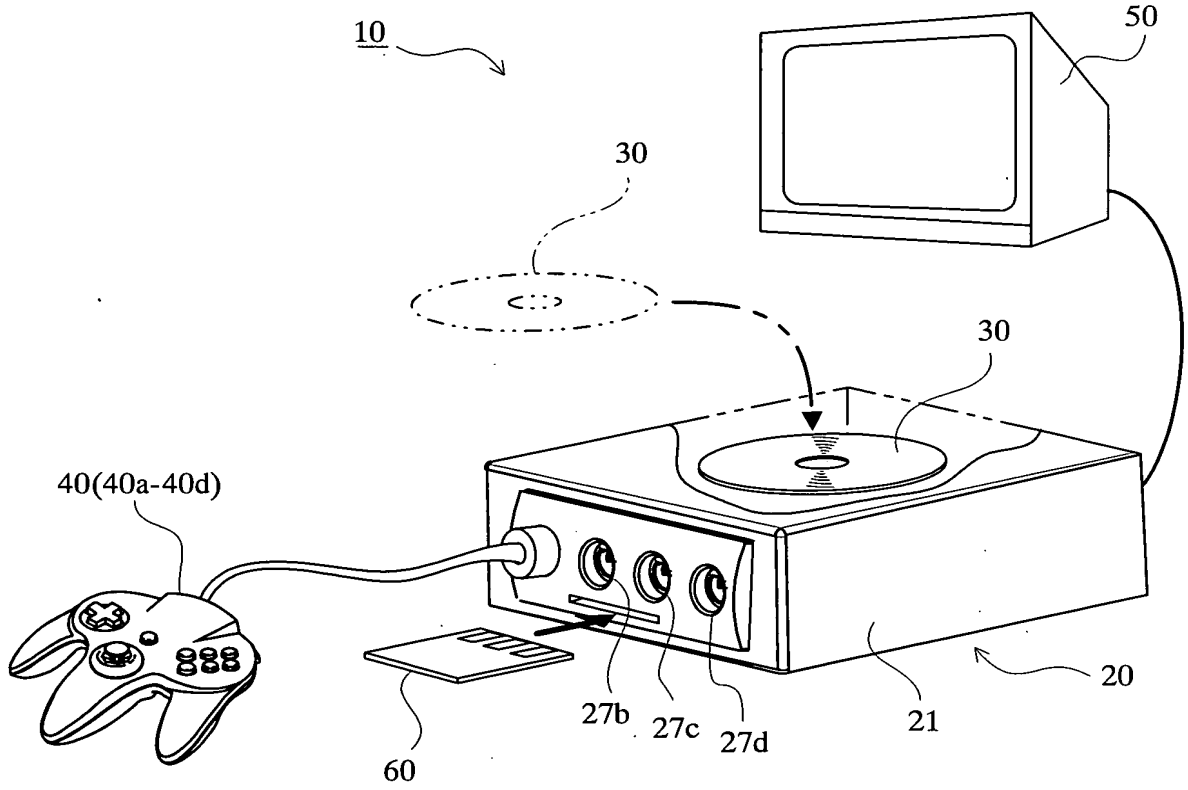

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FIG.2

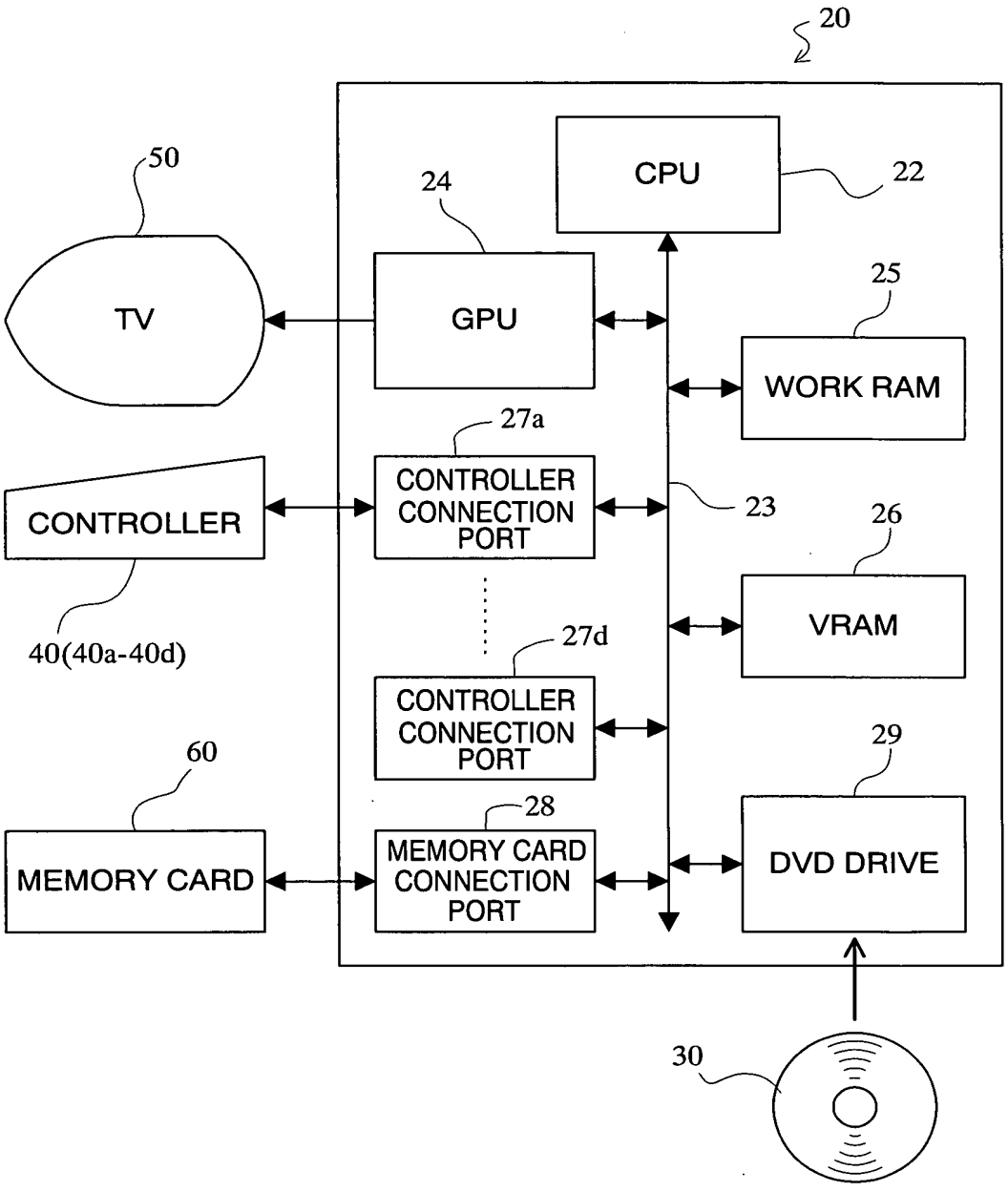

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FIG.3

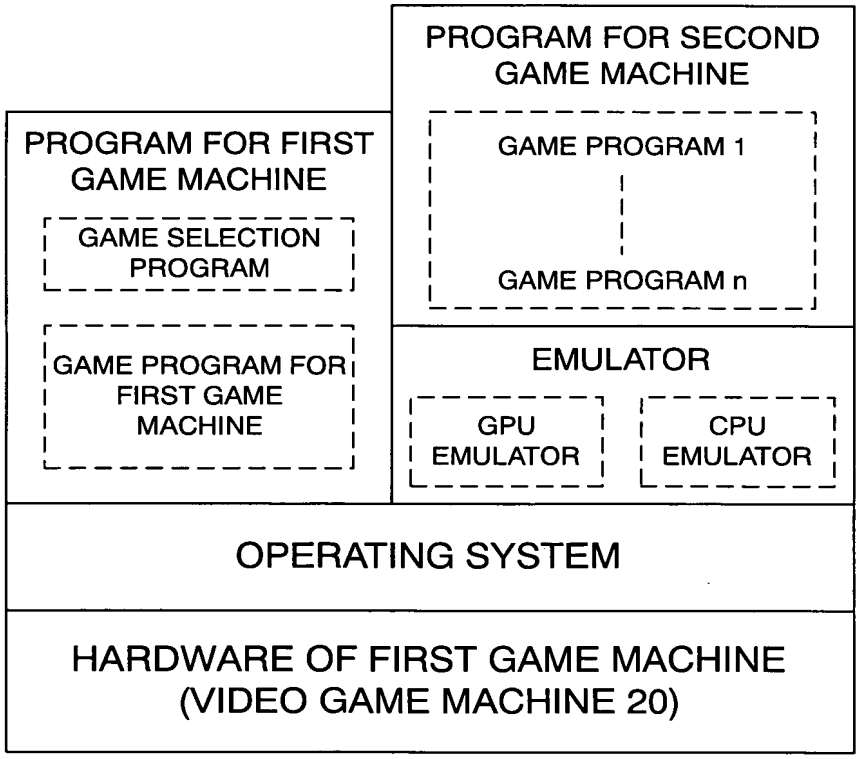

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FIG.4

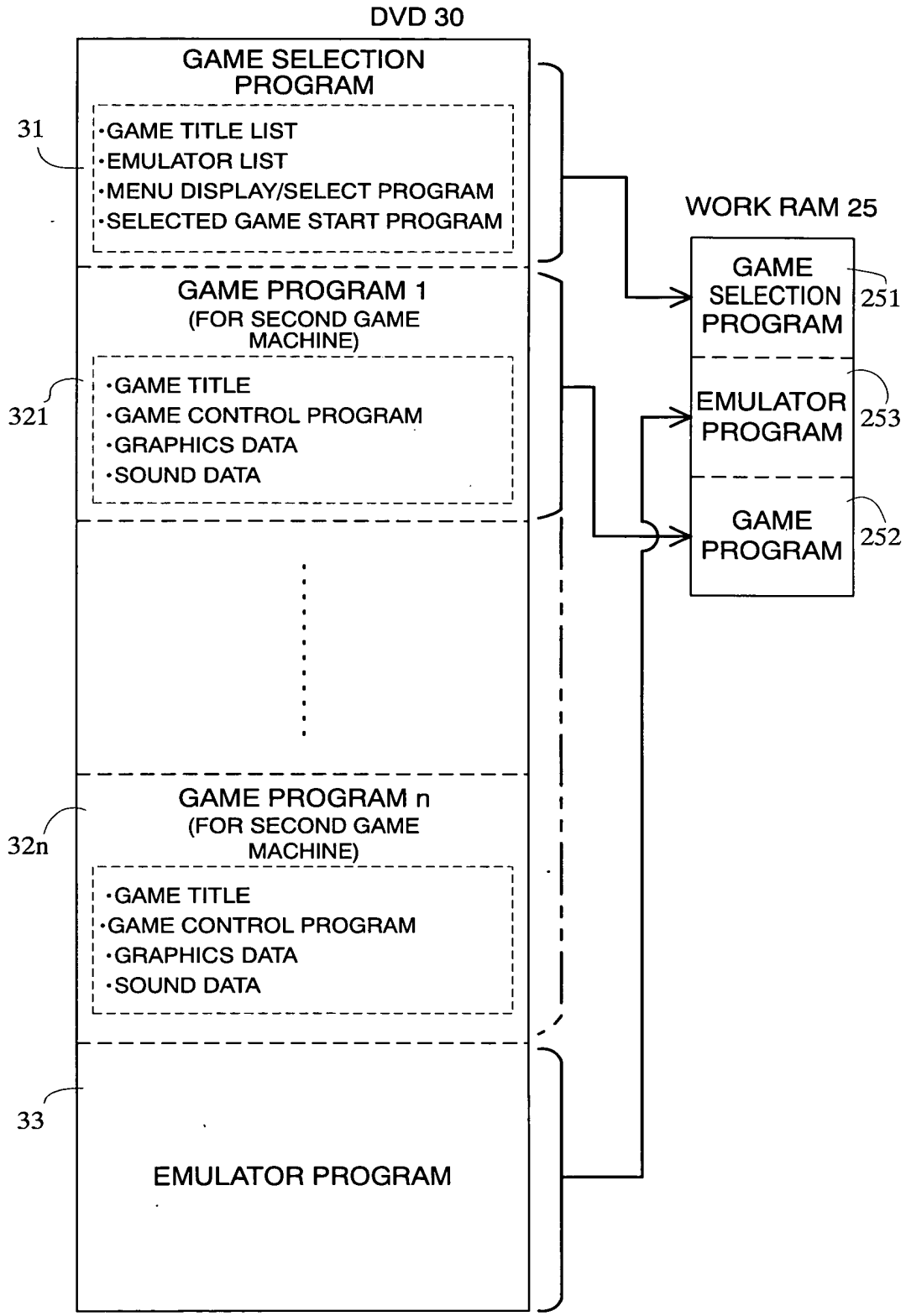

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FIG.5A

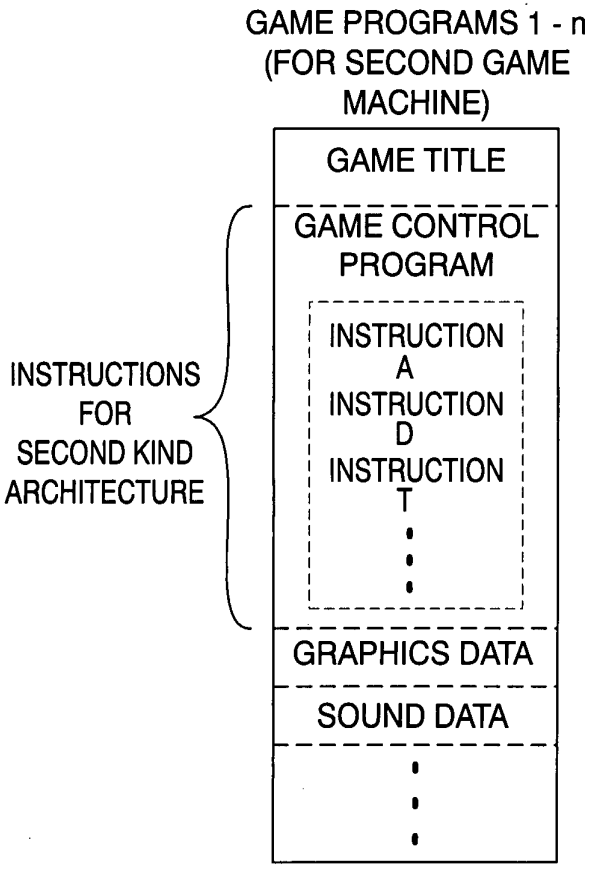

FIG.5B

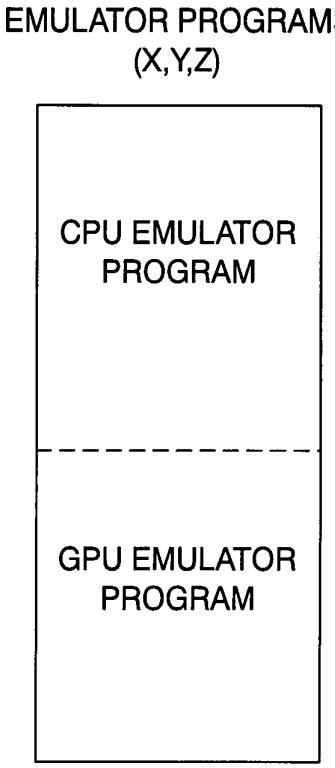

FIG.5C

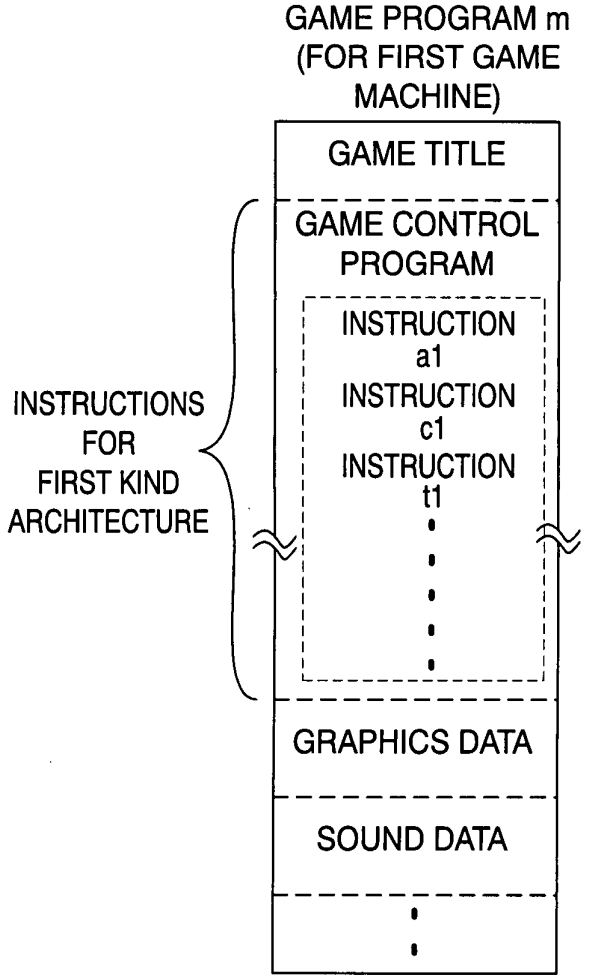

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FIG.6

EMULATOR PROGRAM		
INSTRUCTIONS CONSTITUTING GAME PROGRAM FOR SECOND GAME MACHINE (INSTRUCTIONS OF SECOND KIND ARCHITECTURE)	CPU EMULATOR (INSTRUCTIONS OF FIRST KIND ARCHITECTURE)	GPU EMULATOR (INSTRUCTION OF FIRST KIND ARCHITECTURE)
INSTRUCTION A	PROCESSING a (INSTRUCTIONS a1,a2,a3)	——
INSTRUCTION B	PROCESSING b (INSTRUCTION b1)	——
⋮	⋮	⋮
INSTRUCTION J	PROCESSING j (INSTRUCTIONS j1,j2)	——
INSTRUCTION K + INSTRUCTION L	PROCESSING kl (INSTRUCTION kl1)	——
⋮	⋮	⋮
INSTRUCTION T (EXCLUSIVE INSTRUCTION FOR GPU)	——	PROCESSING t1 {PROCESSING t2 {PROCESSING t3 (INSTRUCTIONS t11,t12, . . .)
INSTRUCTION U (EXCLUSIVE INSTRUCTION FOR GPU)	——	PROCESSING u (INSTRUCTIONS u1,u2,u3)
⋮	⋮	⋮

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FIG.7

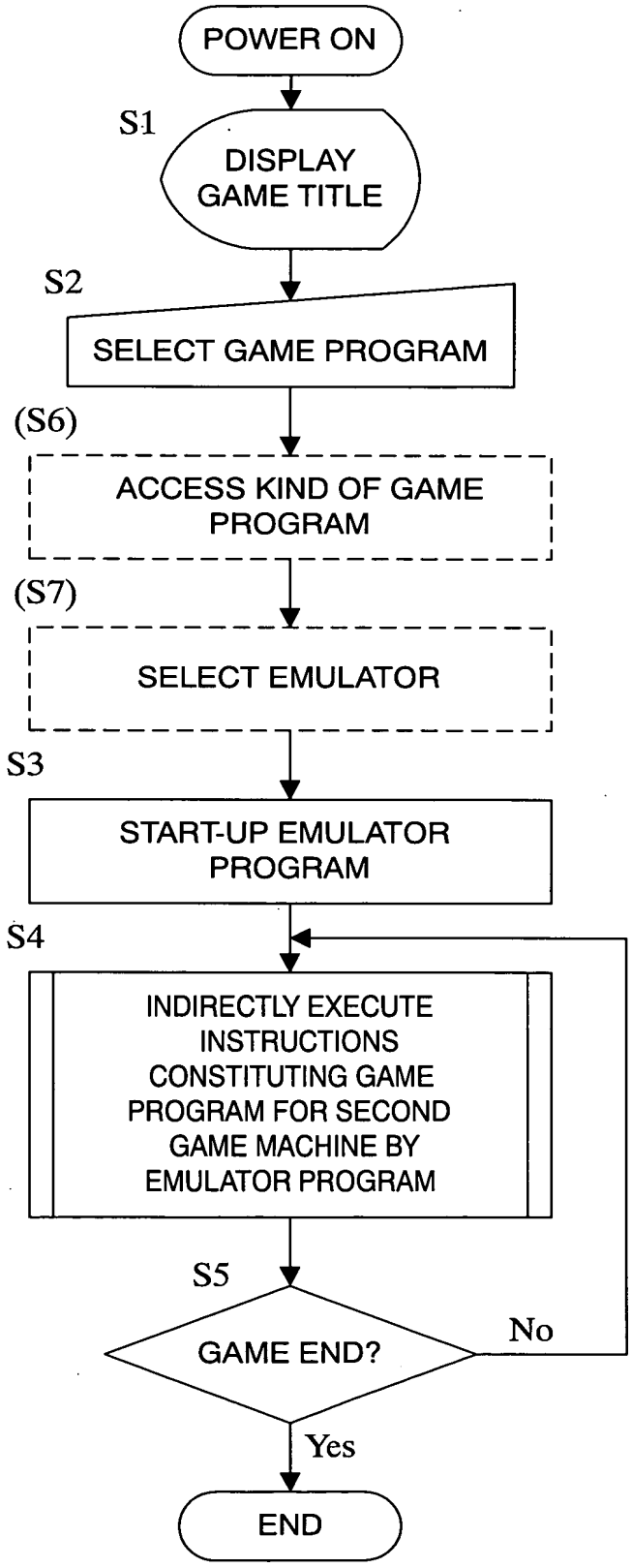

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FIG.8

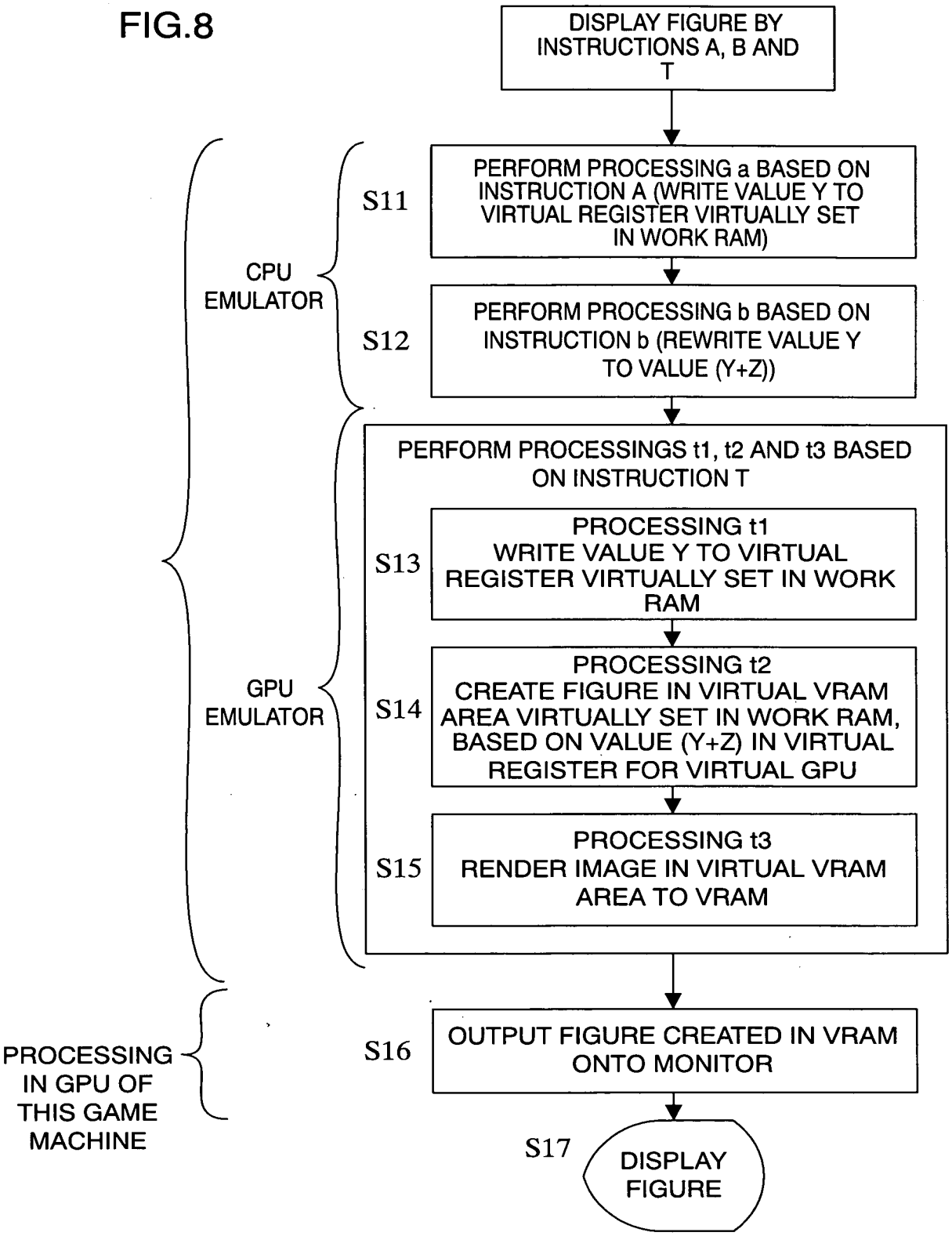

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FIG.9

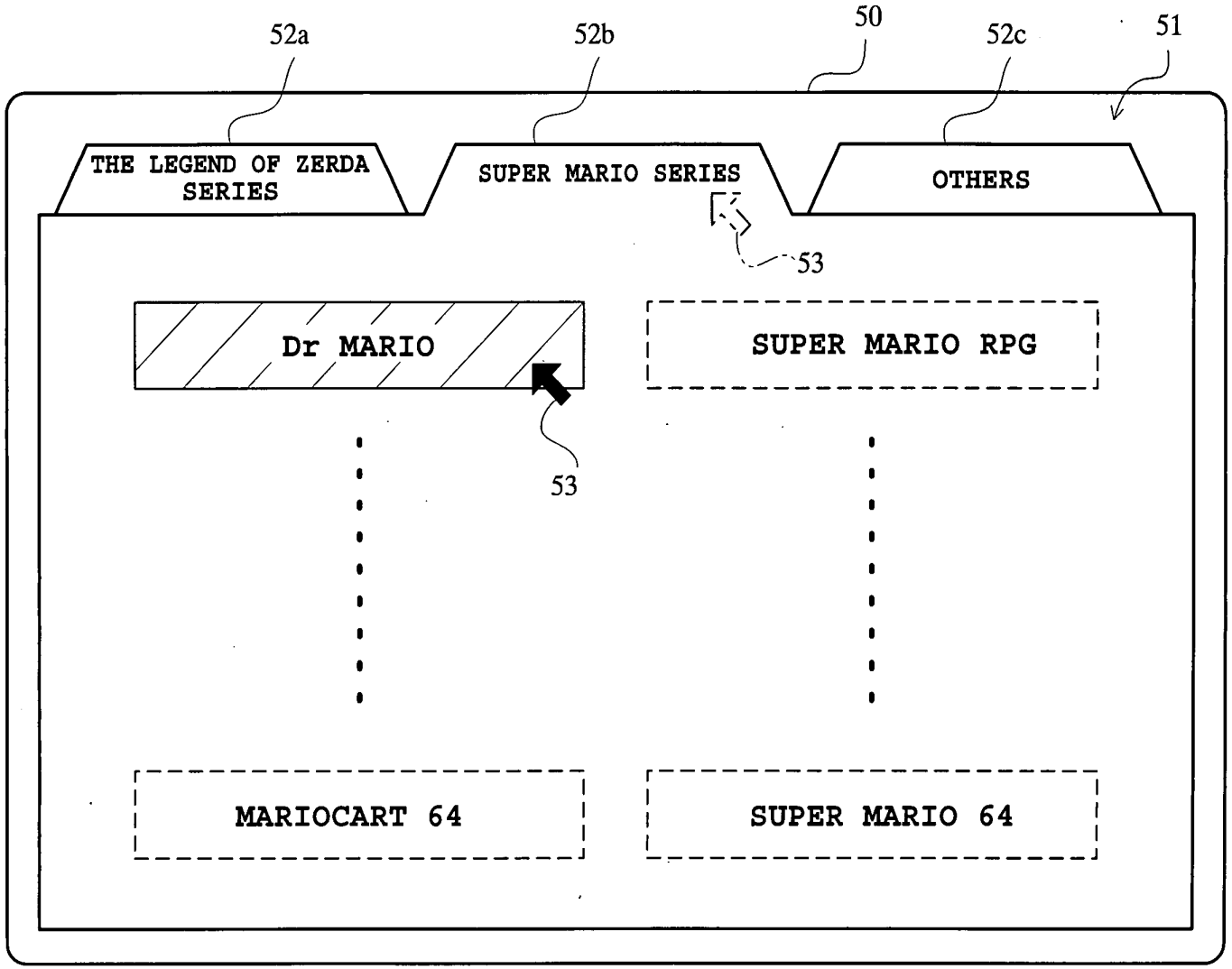

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FIG.10

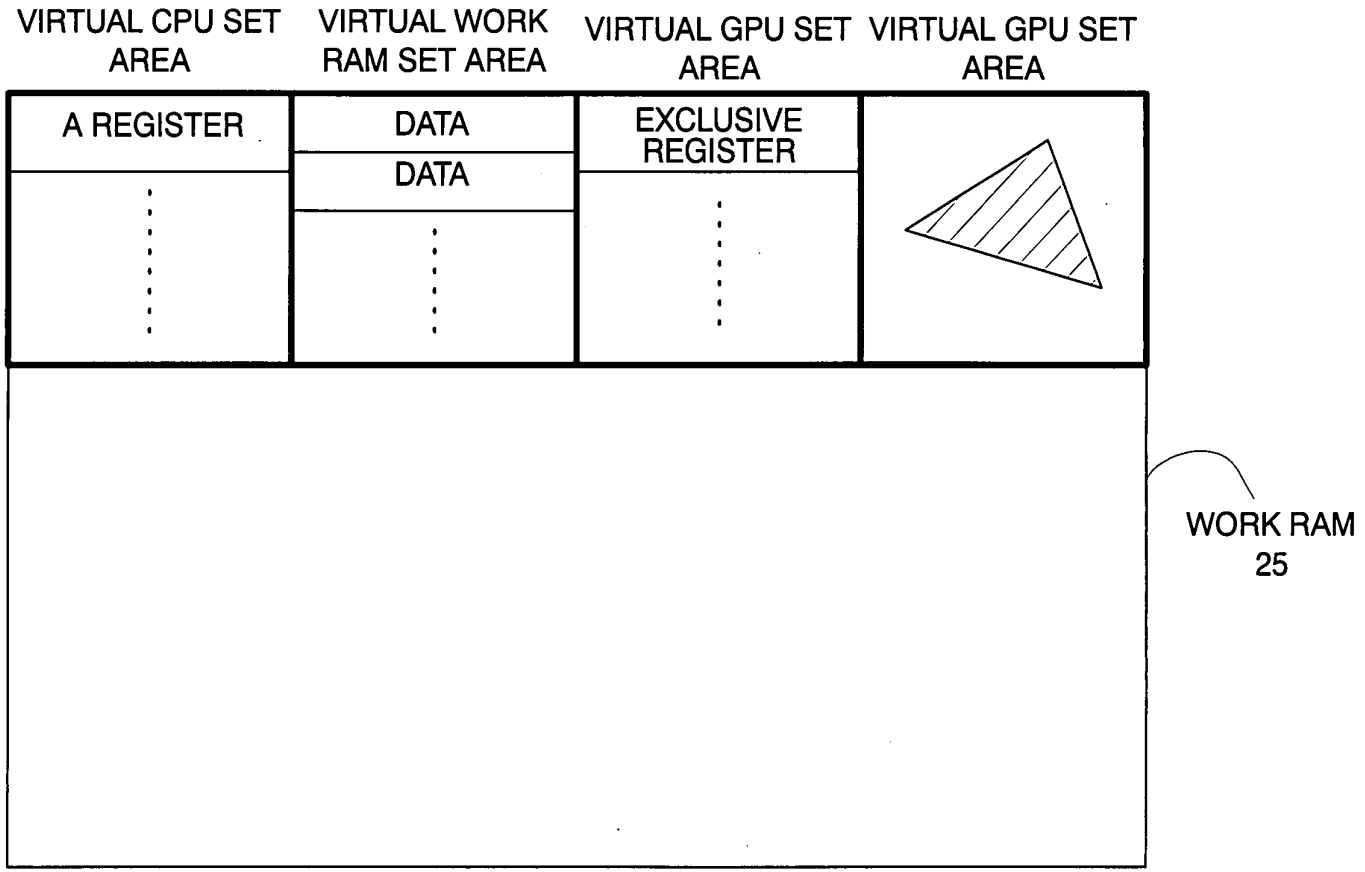

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FIG.11

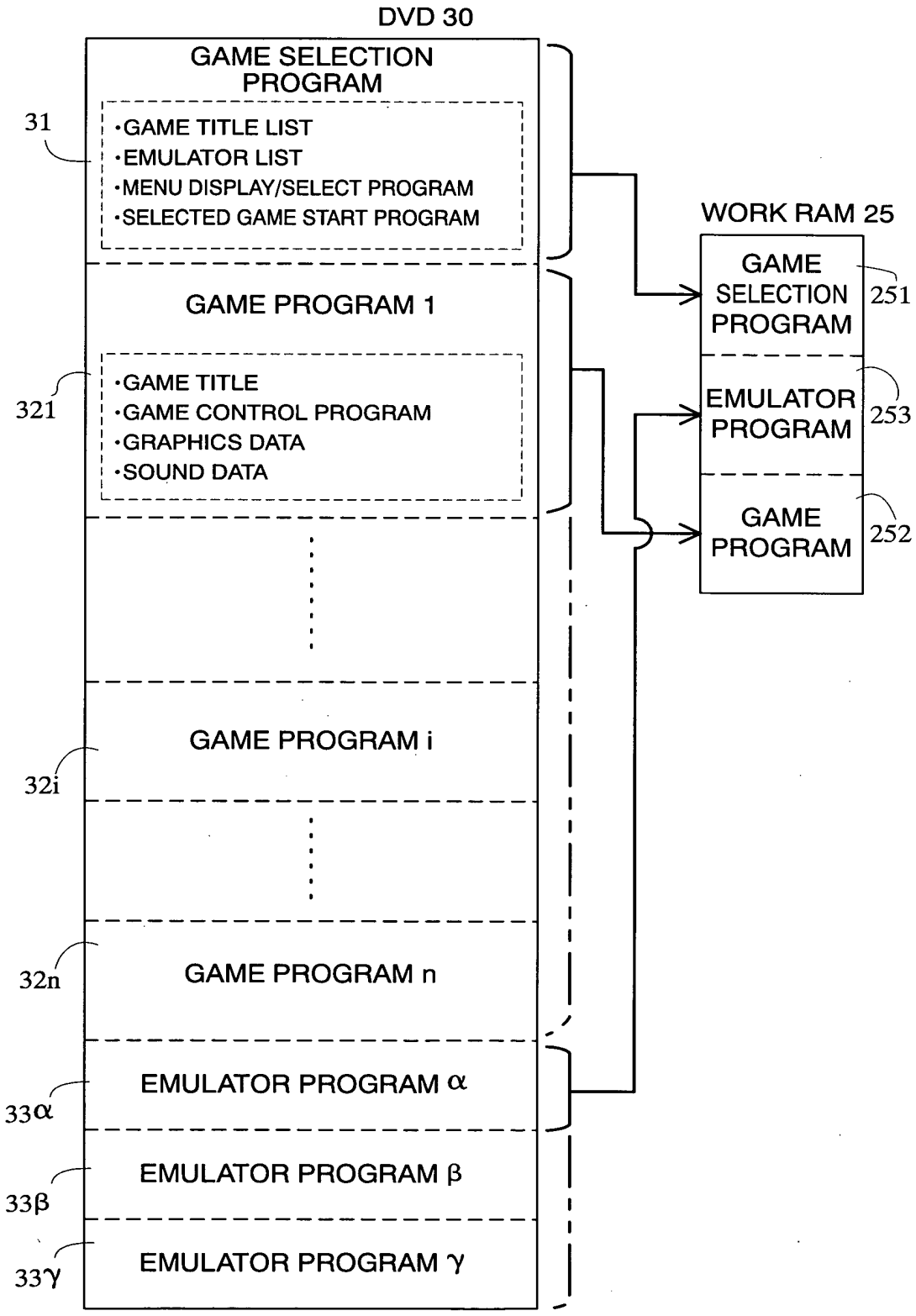

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FIG.12A

GAME SELECTION PROGRAM

GAME TITLE LIST DATA
EMULATOR LIST DATA
MENU DISPLAY/SELECT PROGRAM
SELECTED GAME START PROGRAM

FIG.12B

GAME TILE LIST DATA	EMULATOR LIST DATA
GAME TITLE 1	EMULATOR α
⋮	⋮
GAME TITLE i	EMULATOR β
⋮	⋮
GAME TITLE n	EMULATOR γ
GAME TITLE m	—

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FIG.13

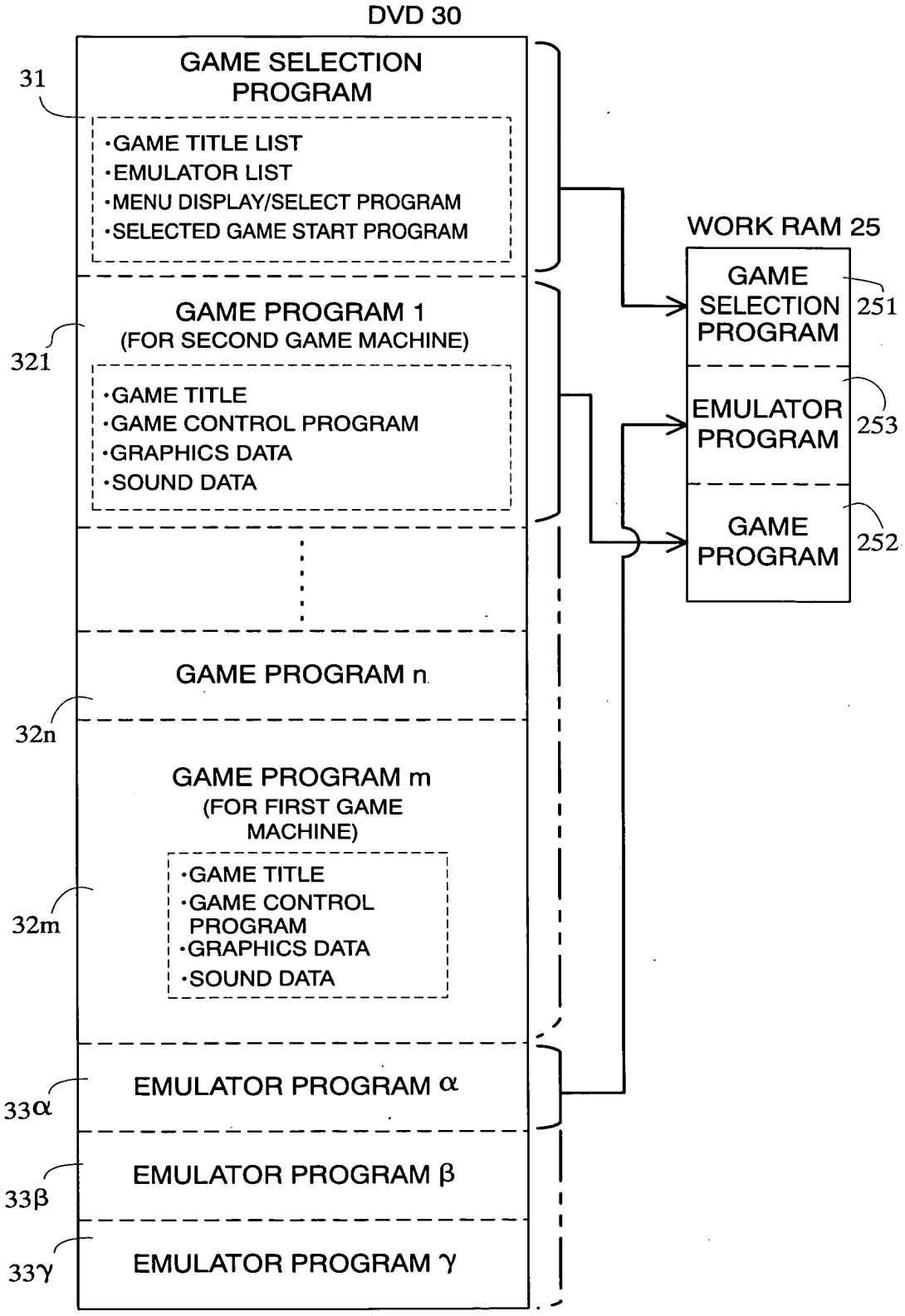

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FIG.14

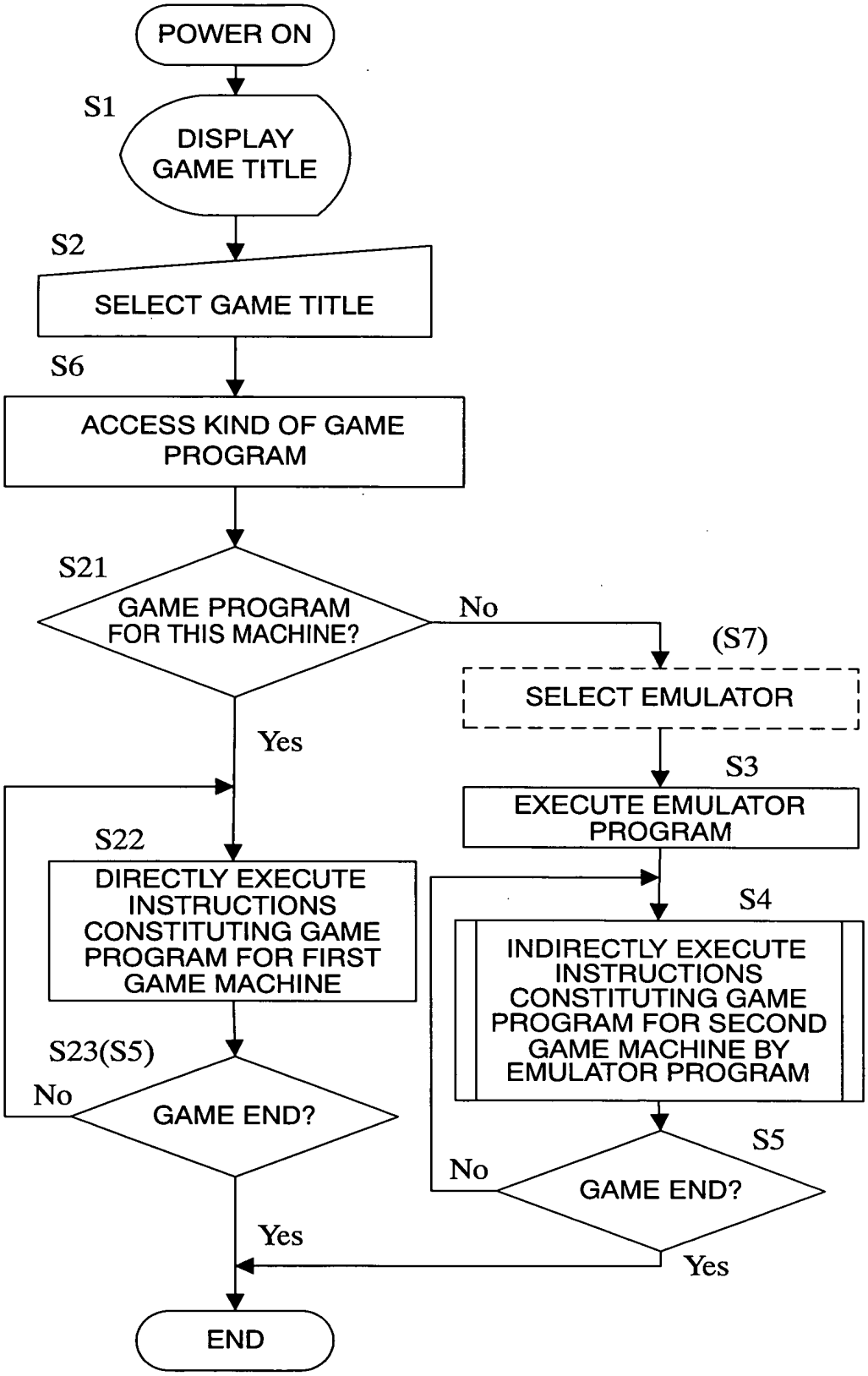

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FIG.15

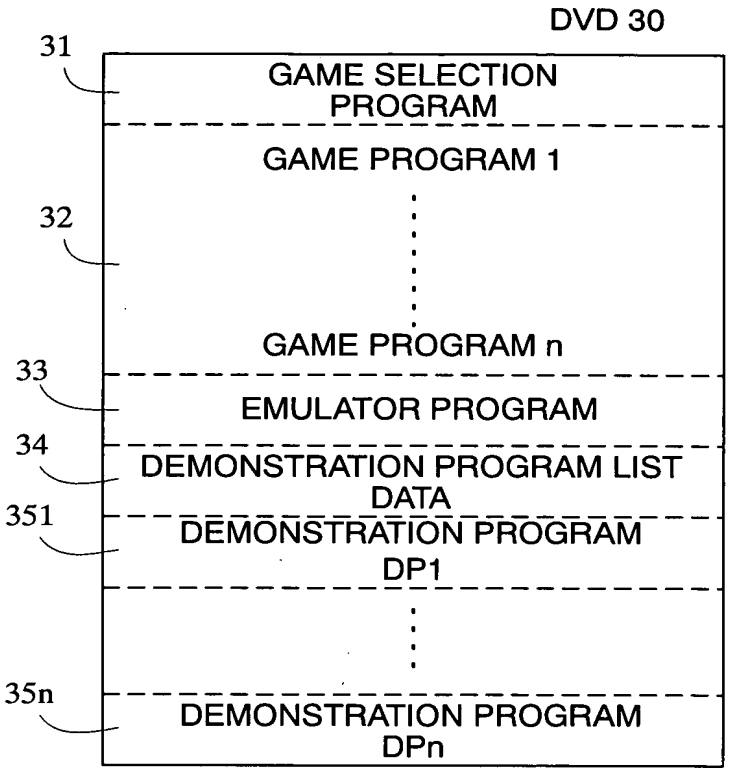

FIG.16

DEMONSTRATION PROGRAM LIST DATA

GAME TITLE	DEMONSTRATION PROGRAM
GAME TITLE 1	DEMONSTRATION PROGRAM DP1
⋮	⋮
GAME TITLE i	DEMONSTRATION PROGRAM DP i
⋮	⋮
GAME TITLE n	DEMONSTRATION PROGRAM DPn

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FIG.17

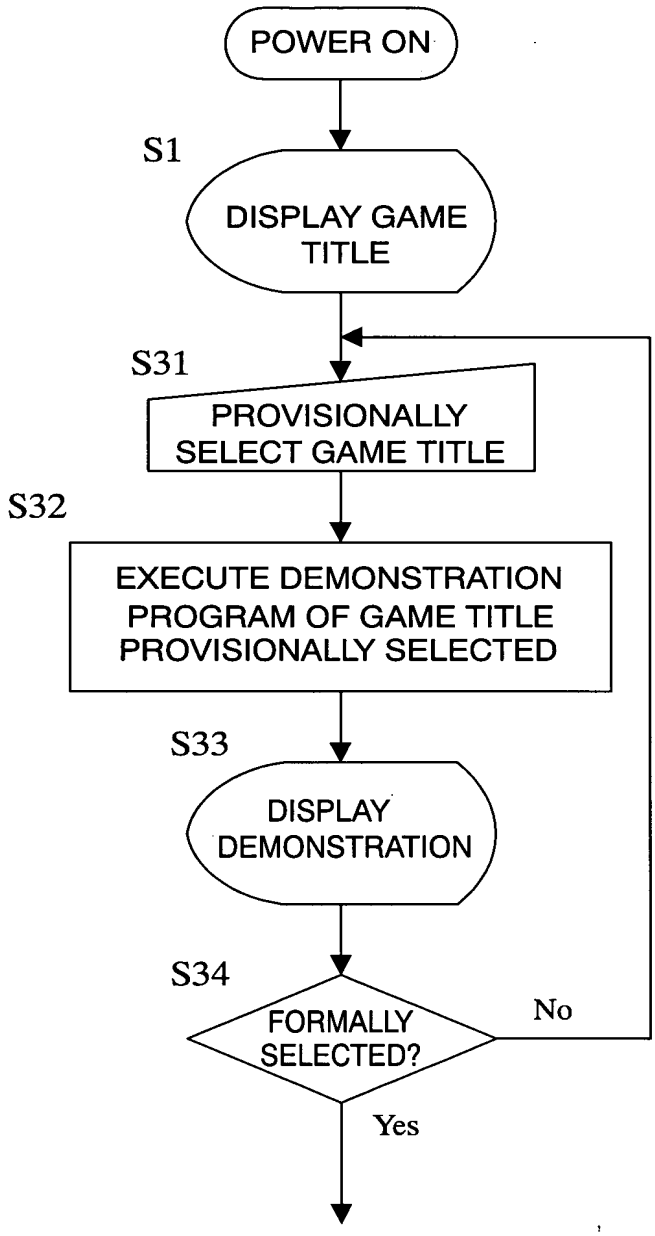

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FIG.18

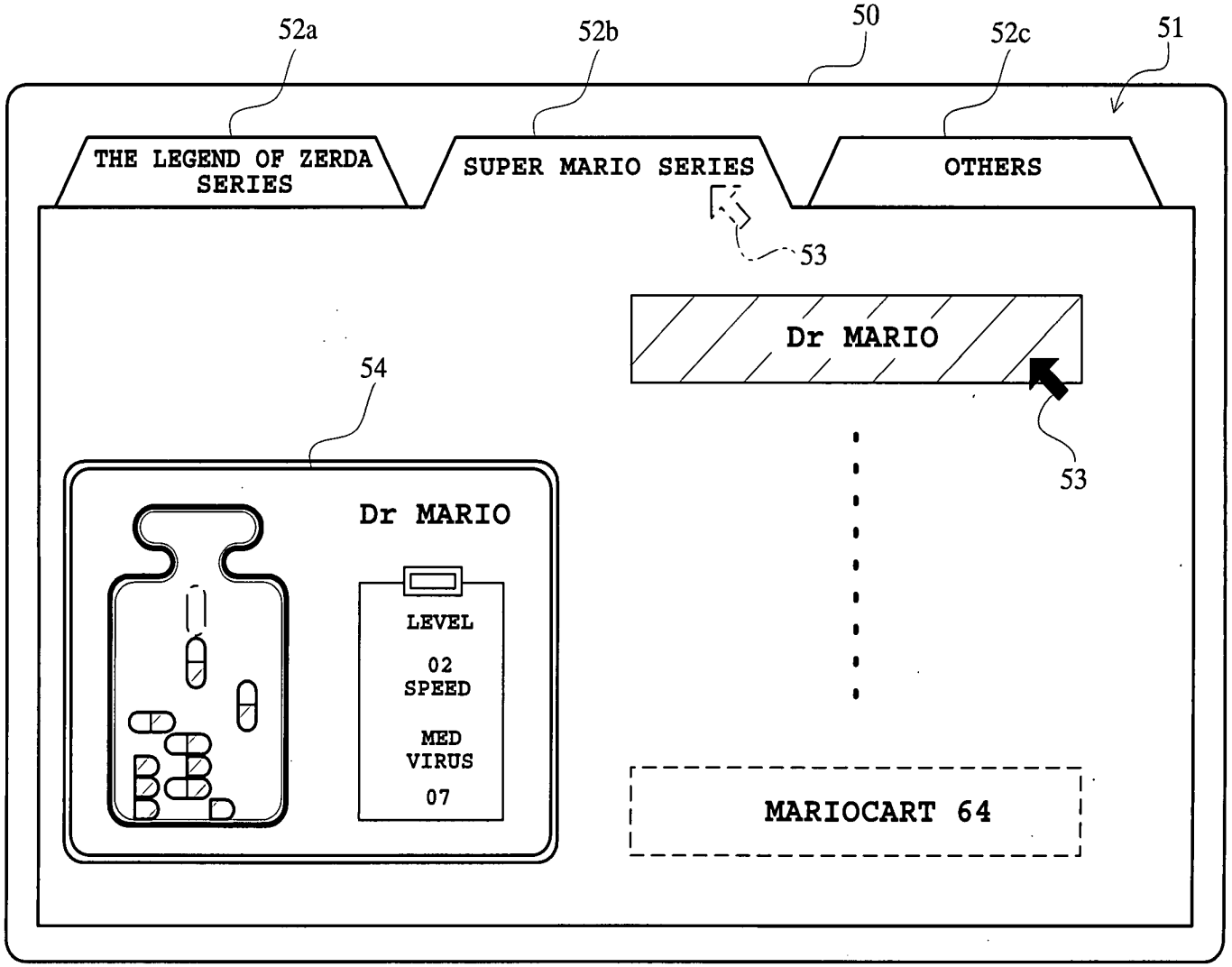

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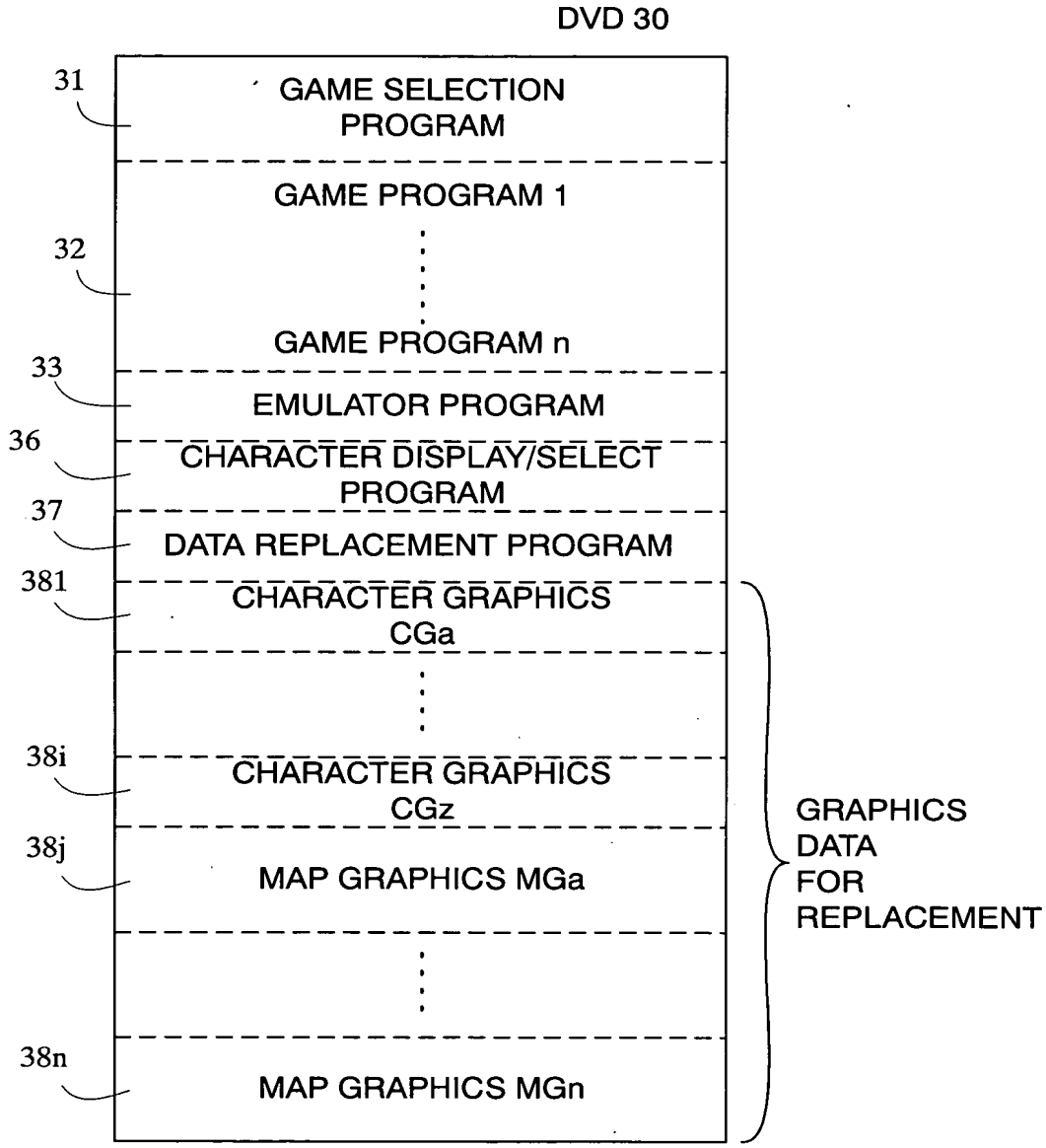
FIG.19



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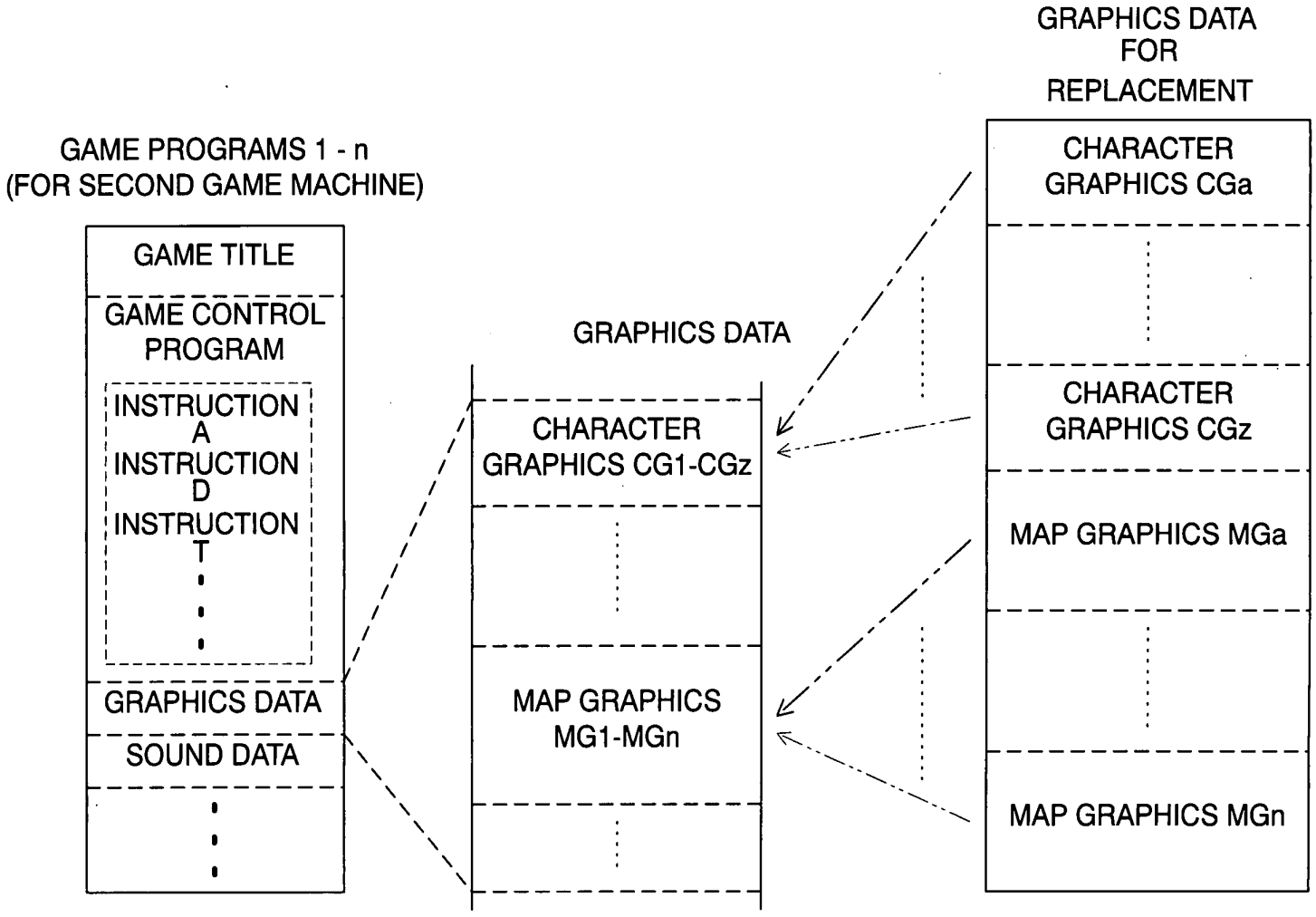

FIG.20A FIG.20B FIG.20C

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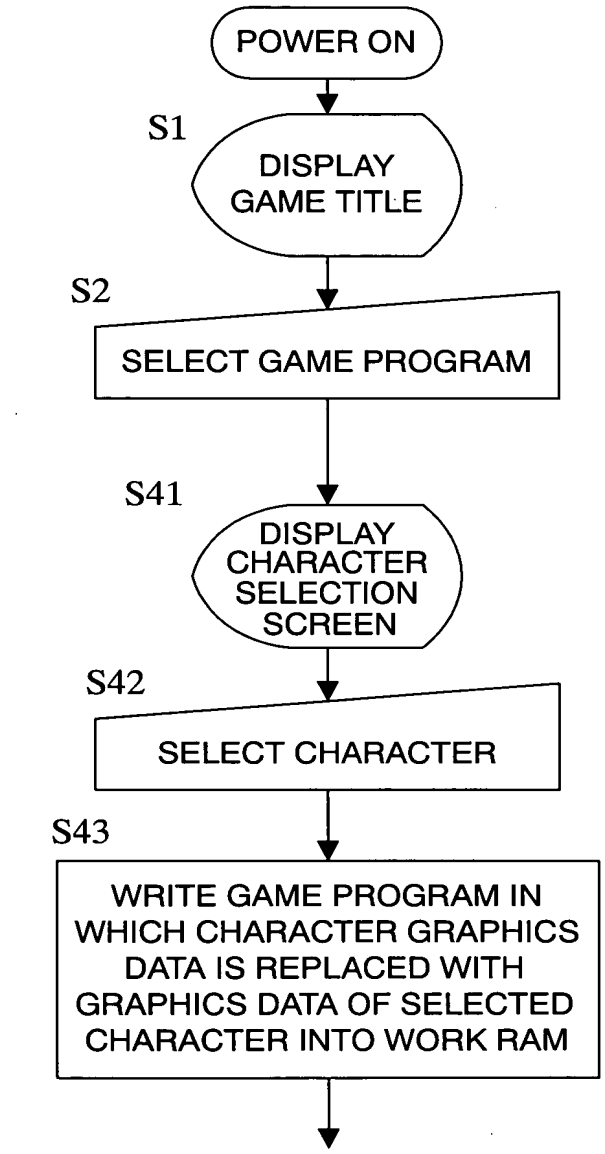
FIG.21



SAME AS S3(S6)-S5 OF FIGURE 7

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FIG.22

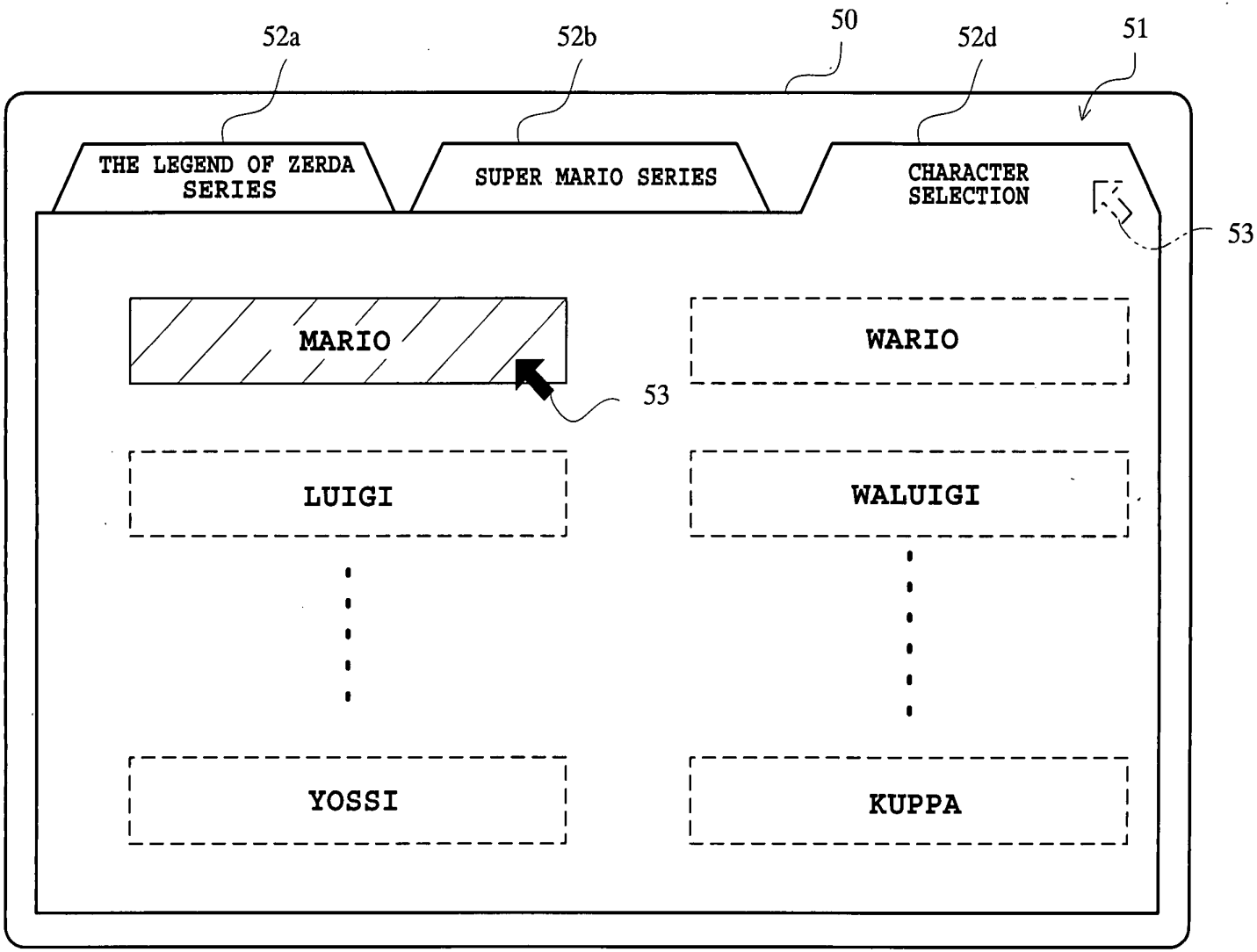

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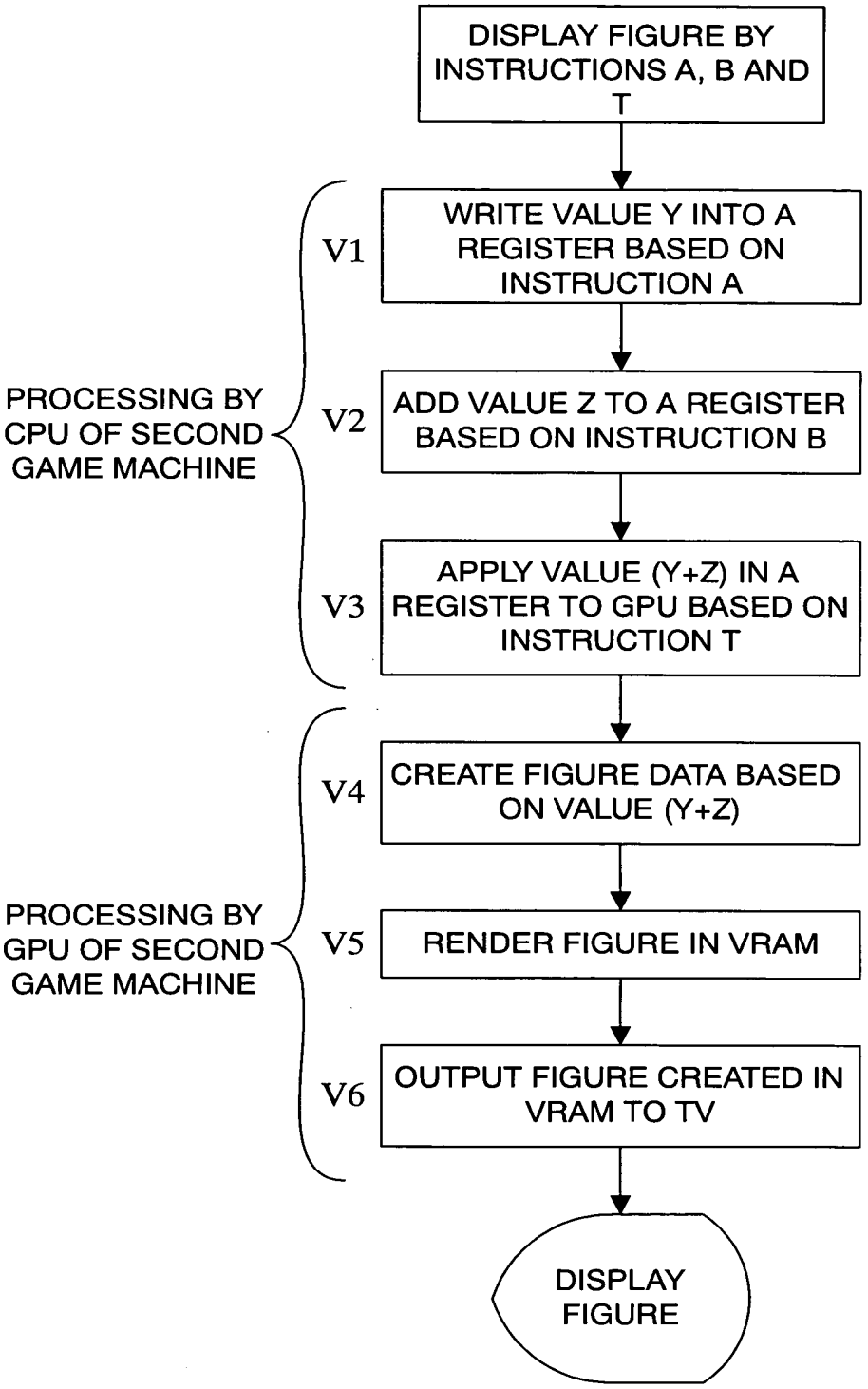
FIG.23



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